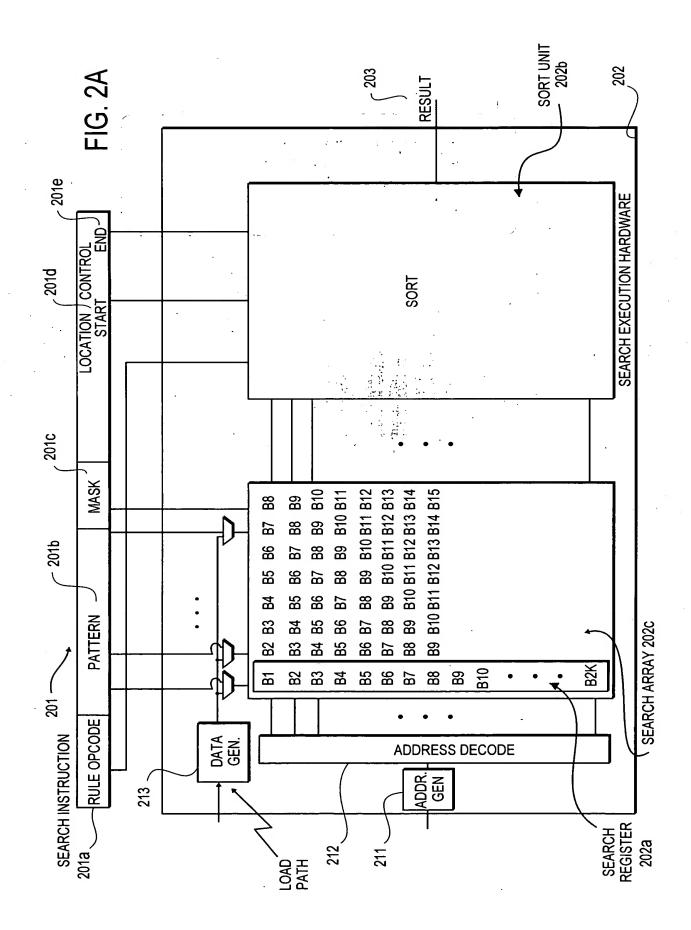
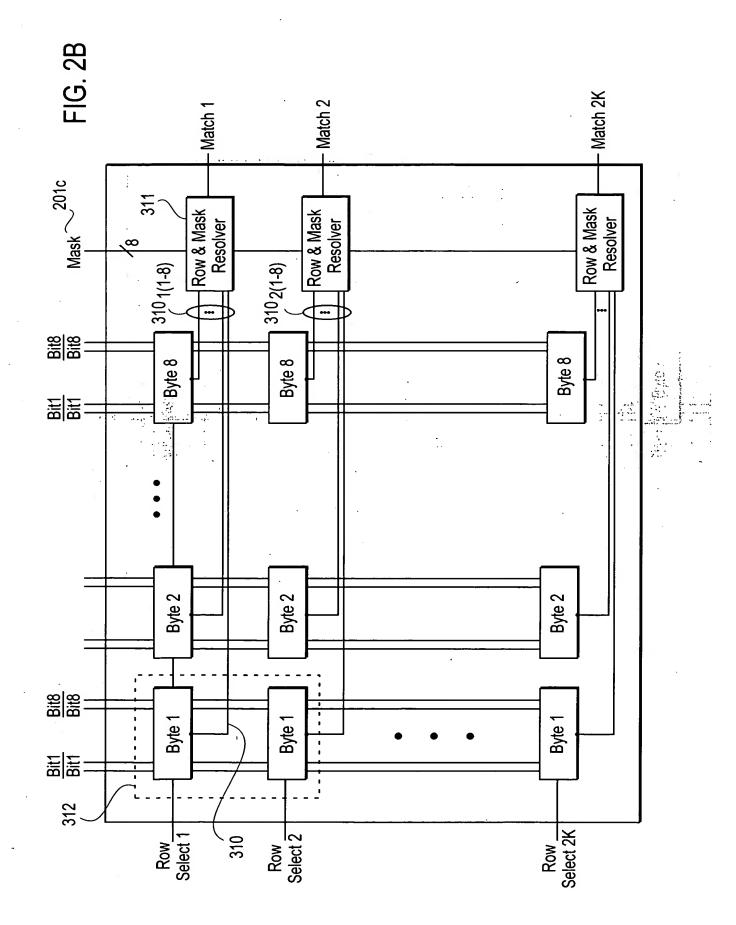
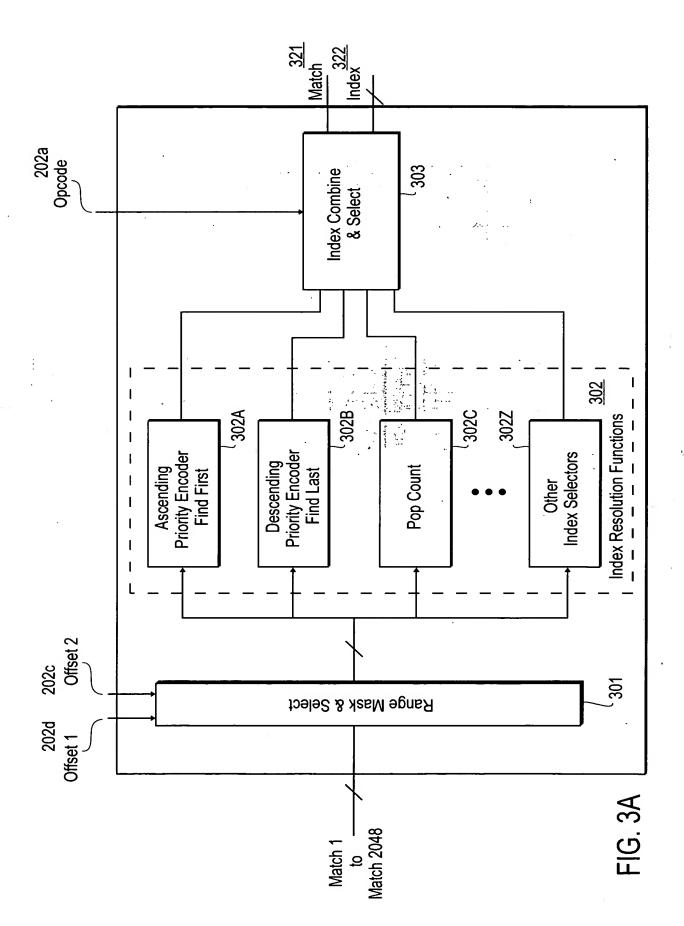


FIG. 1







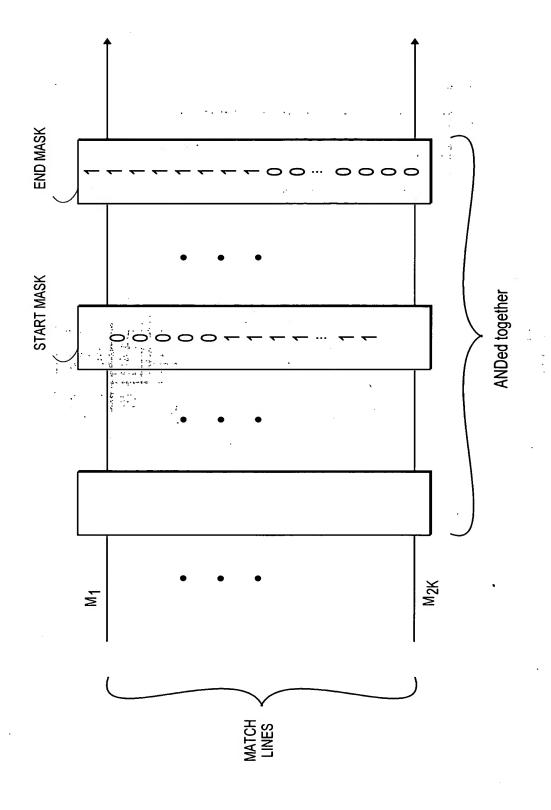
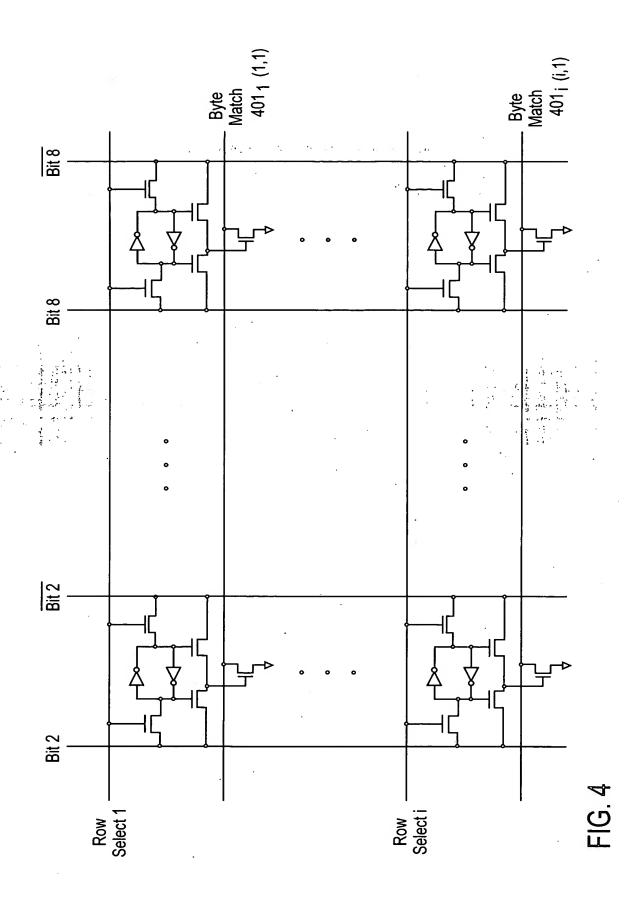
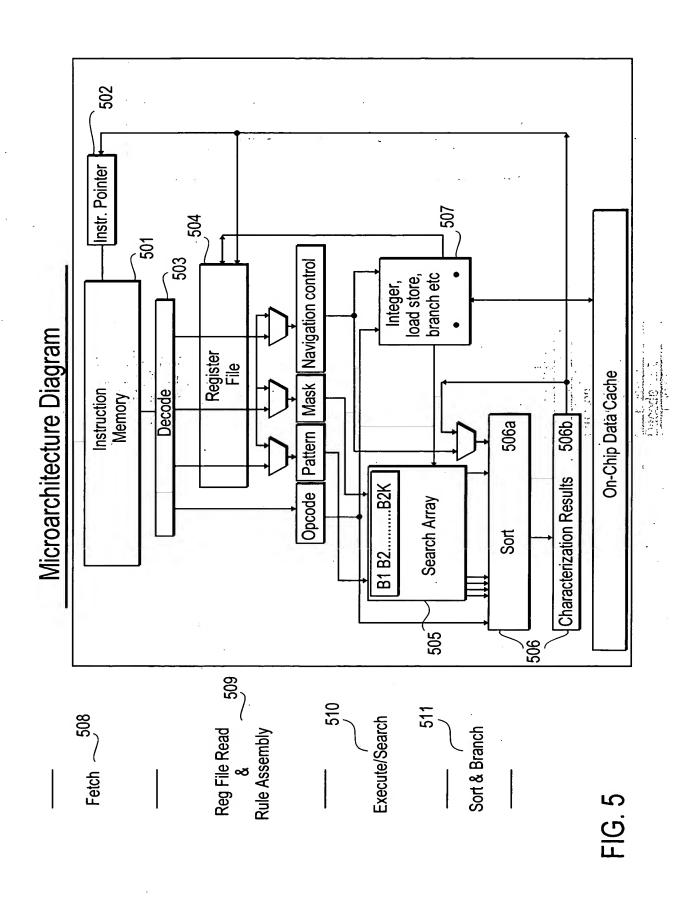


FIG. 3B





Example pseudo-code and Rule Engine micro-code

Pseudo-code

FIND (("cp /bin/sh /usr/spool/mail/root" BEFORE "chmod 4755 !\$") AND "touch") BEFORE "mail"

Micro-code for an exemplary Rule Engine

602

Branch Address OXB OXB OXB OXB 0XB OXB OXB OXB OXB OXB OXB 0XB 8 8 S S S S S S Result Register × ပ ပ Ă Ø ×  $\mathbf{\omega}$ Indirect End Offset 0 0 0 0x800 0x800 0x800 0x800 0x800 0x800 0x800 0x800 0x800 End Offset Start Start Offset Offset Indirect 0 0 <B + 5> <4 + 7> <4 + 8> <¥ + 8> <4 + 8> <4 + 5> 0x03D 0x03D 区 OXFF OXFF Mask 0XF8 OXF0 OXFF 0XFF 0XFF 0xF8 0XF0 "chmod 47" "spool/ma" "cp /bin/" Pattern "sh /usr/" "tonch" "il/root" **..**22 i&.. "mail" "mail" FIND\_FORWARD\_ANCHORED FIND\_FORWARD\_ANCHORED FIND\_FORWARD\_ANCHORED FIND\_FORWARD\_ANCHORED CMP(B+5, A+5)\_BRANCH(10) FIND\_FIRST\_FORWARD FIND\_FIRST\_FORWARD FIND\_FIRST\_FORWARD FIND\_FIRST\_FORWARD FIND\_FIRST\_FORWARD Opcode 9 വ 9 ω တ

603

604

FIG. 6

THE POST OF

Execution of example micro-code: pipeline diagram

Clock 14	ı	ı	1	10
Clock 13	l	ı	10	ı
Clock 12	1.	10	1	1 *
Clock 11	10	ı	ı	
Clock 10	1	1	ı	&
Clock 9	1,	1.	8	7
Clock 8	11	8	7	9
Clock 7	8	2	9.	9
Clock 7	2	9	5	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
Clock 6	9	2	4	3 3 3 3 4 3 4 3 4 3 4 3 4 3 4 3 4 3 4 3
Clock 5	5	4	. €	2
Clock 4	4	3	2	
Clock 3	3	2	. 1	-
Clock 2	2	-	1	ı
Clock 1	1	ı	1	ı
701	Fetch	Assemble	Search/ Execute	Sort & Branch